

### • General Description

The CH10P04N combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . This device is ideal for load switch and battery protection applications.

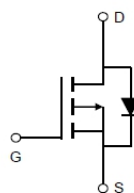
### • Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

### • Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

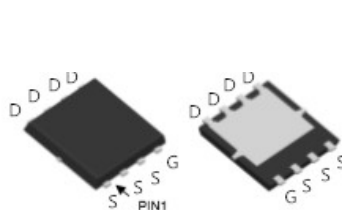
### • Product Summary



$$V_{DS} = -40V$$

$$R_{DS(ON)} = 33m\Omega$$

$$I_D = -10A$$



PDFN3\*3



### • Ordering Information:

Part NO.	CH10P04N
Marking	CH10P04N
Packing Information	REEL TAPE
Basic ordering unit (pcs)	5000

### • Absolute Maximum Ratings ( $T_c = 25^\circ C$ )

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_{D@TC=25^\circ C}$	-10	A
	$I_{D@TC=75^\circ C}$	- 8	A
	$I_{D@TC=100^\circ C}$	-5	A
Pulsed Drain Current <sup>①</sup>	$I_{DM}$	-30	A
Total Power Dissipation	$P_D@TC=25^\circ C$	4.6	W
Total Power Dissipation	$P_D@TA=25^\circ C$	1.5	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ C$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ C$
Single Pulse Avalanche Energy	$E_{AS}$	160	mJ

**•Thermal resistance**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}$	-	-	2.3	$^{\circ}C/W$
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	27	$^{\circ}C/W$
Soldering temperature, wavesoldering for 10s	$T_{sold}$	-	-	265	$^{\circ}C$

**•Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = -250\mu A$	-1.0	-1.6	-2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS} = -40V, V_{GS} = 0V$			-1.0	$\mu A$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -5A$		33	43	$m\Omega$
		$V_{GS} = -4.5V, I_D = -3A$		44	60	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = -10V, I_D = -5A$		10		s
Source-drain voltage	$V_{SD}$	$I_S = -5A$			1.28	V

**•Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	$C_{iss}$	f = 1MHz	-	1034	-	pF
Output capacitance	$C_{oss}$		-	107	-	
Reverse transfer capacitance	$C_{rss}$		-	79.5	-	

**•Gate Charge characteristics( $T_a = 25^{\circ}C$ )**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	$Q_g$	$V_{DD} = -20V$	-	20	-	nC
Gate - Source charge	$Q_{gs}$	$I_D = -5A$	-	3.5	-	
Gate - Drain charge	$Q_{gd}$	$V_{GS} = -10V$	-	3.3	-	

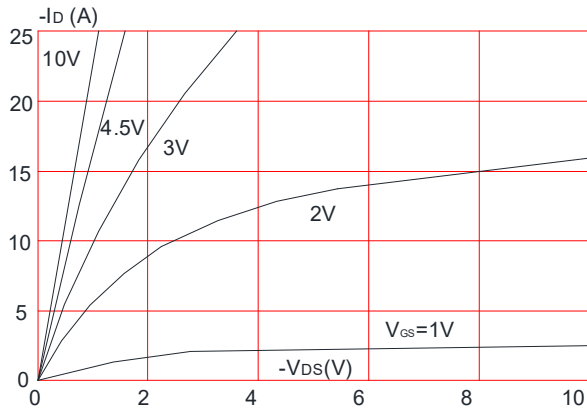
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition:  $T_J = 25^{\circ}C, V_{DD} = -20V, V_G = -10V, L = 0.5mH, R_G = 25\Omega, I_{AS} = -10A$

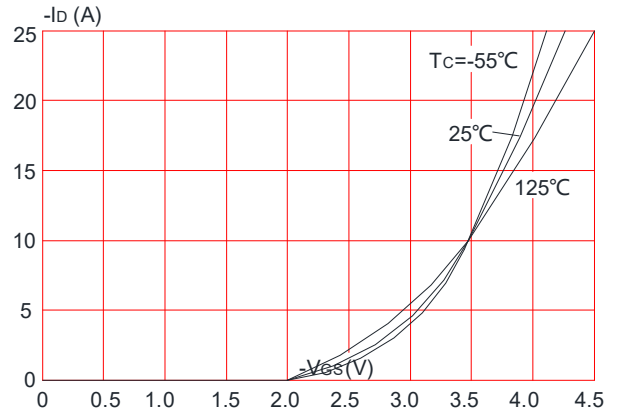
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$

## Typical Performance Characteristics

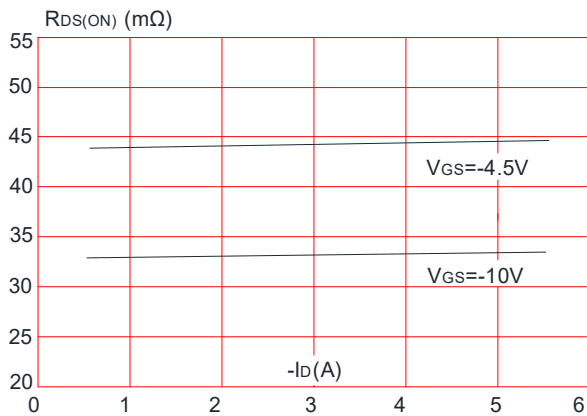
**Figure 1: Output Characteristics**



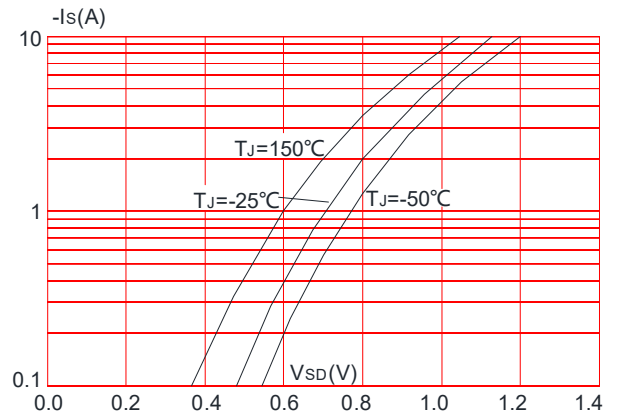
**Figure 2: Typical Transfer Characteristics**



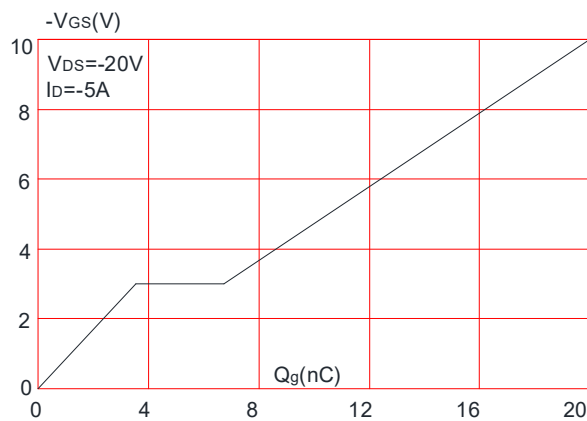
**Figure 3: On-resistance vs. Drain Current**



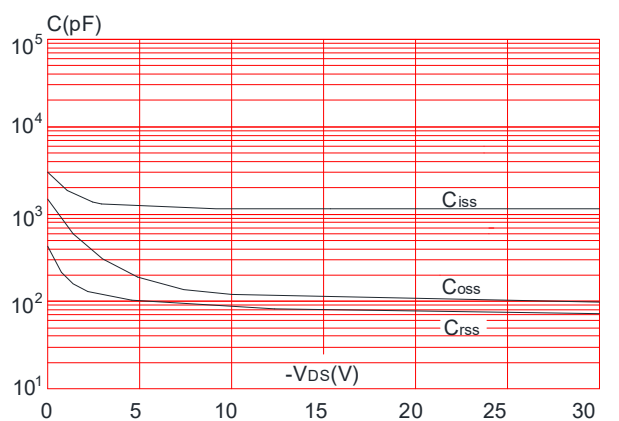
**Figure 4: Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**

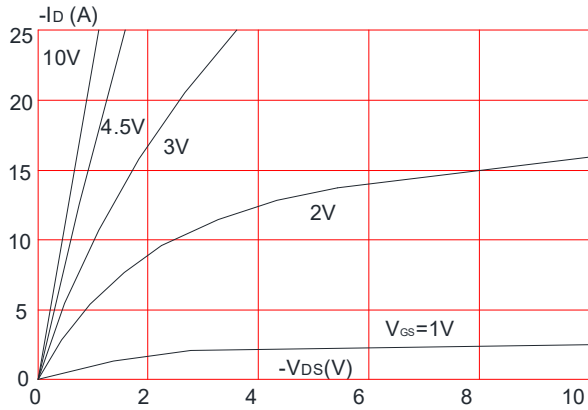


**Figure 6: Capacitance Characteristics**

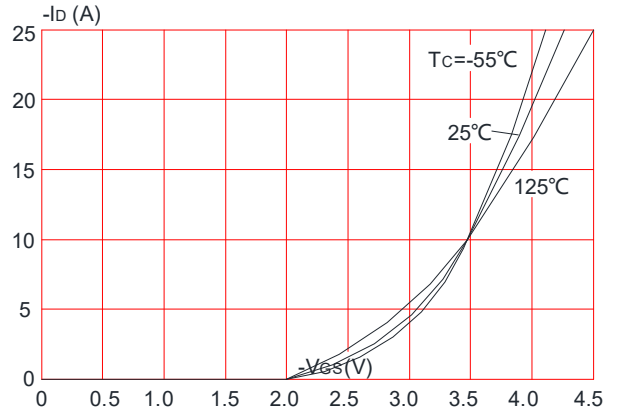


## Typical Performance Characteristics

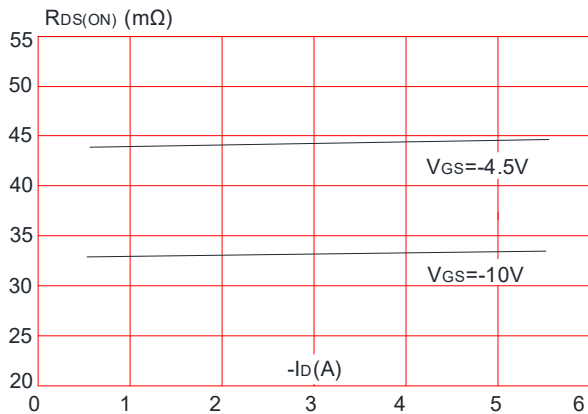
**Figure 1: Output Characteristics**



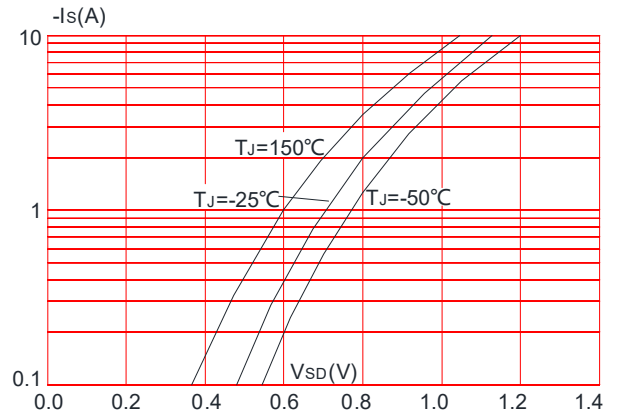
**Figure 2: Typical Transfer Characteristics**



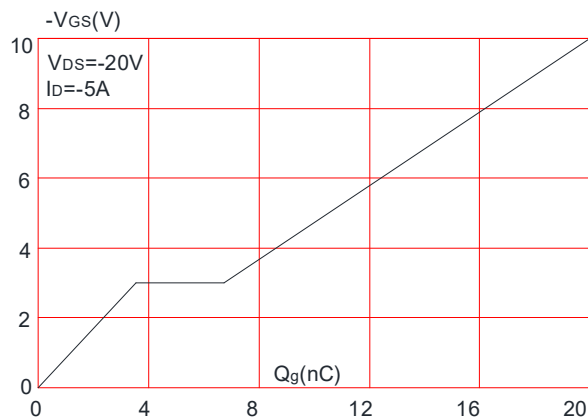
**Figure 3: On-resistance vs. Drain Current**



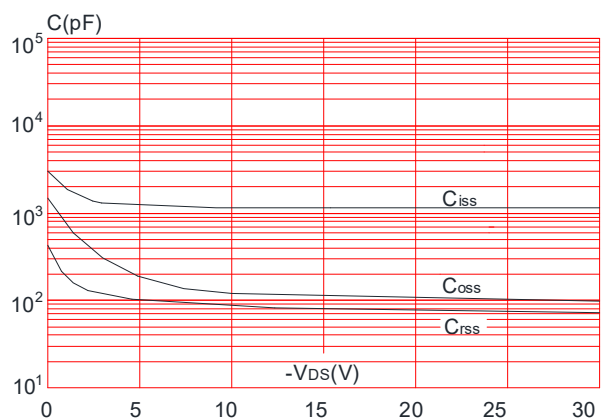
**Figure 4: Body Diode Characteristics**



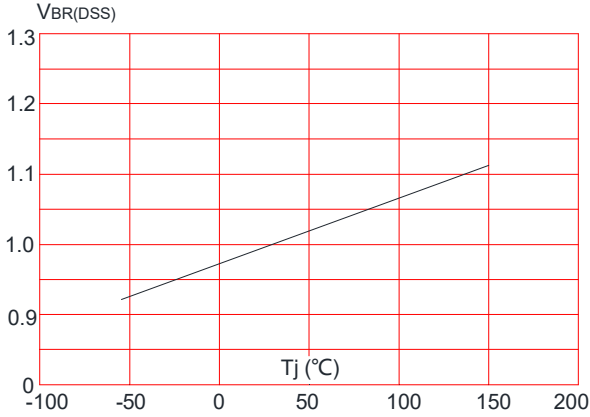
**Figure 5: Gate Charge Characteristics**



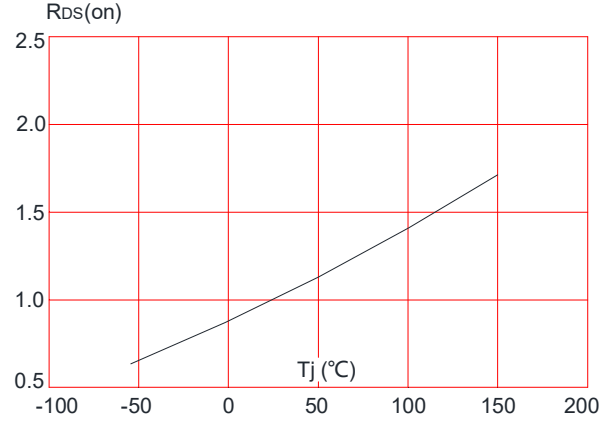
**Figure 6: Capacitance Characteristics**



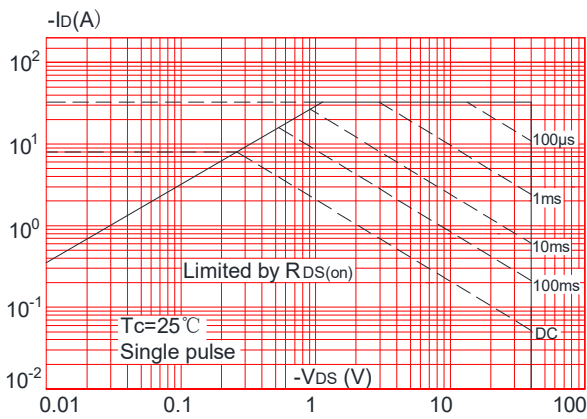
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



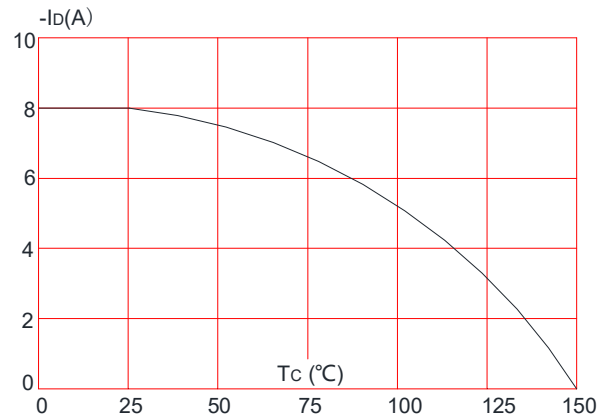
**Figure 8: Normalized on Resistance vs. Junction Temperature**



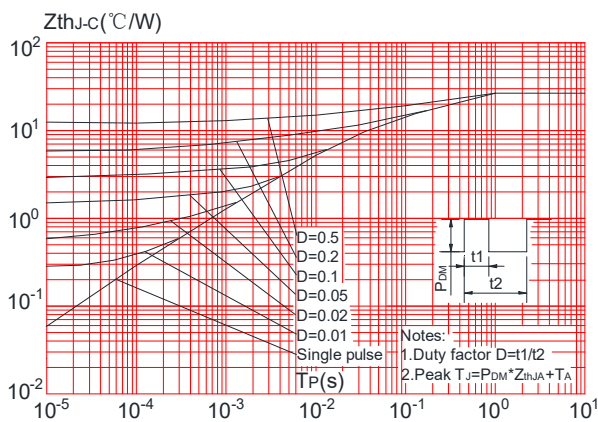
**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**

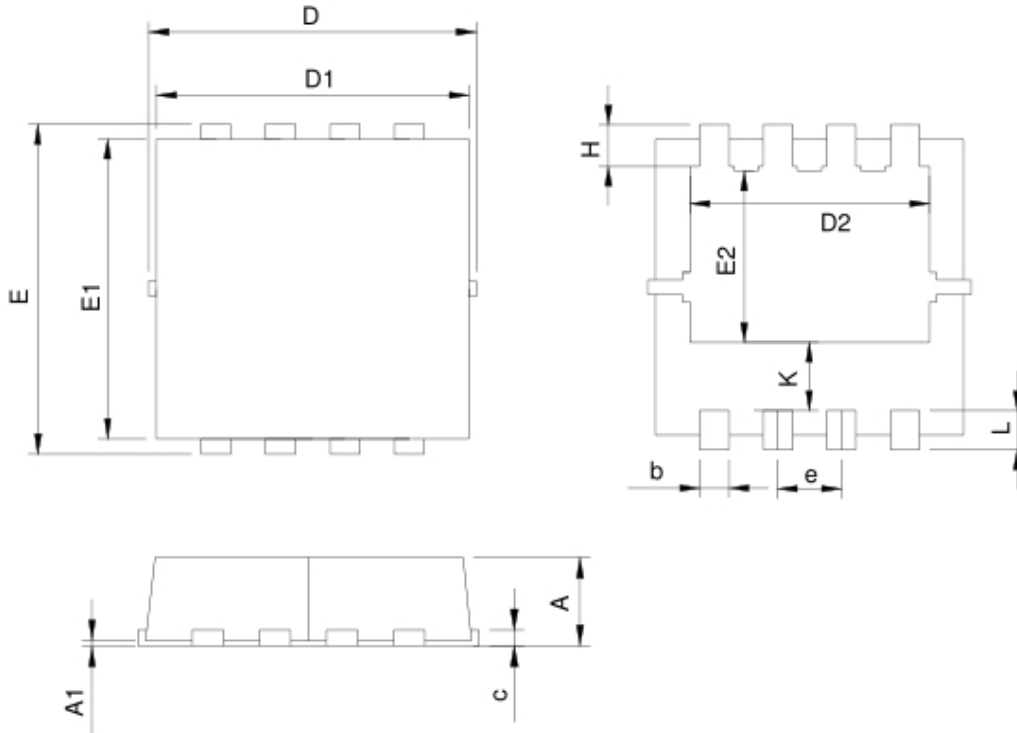


**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case**



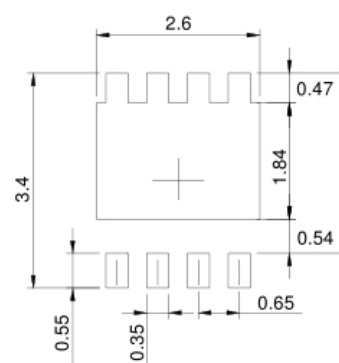
•Dimensions (DFN3×3)

Unit : mm



SYMBOL	DFN3.3x3.3-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.00	0.028	0.039
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
c	0.14	0.20	0.006	0.008
D	3.10	3.50	0.122	0.138
D1	3.05	3.25	0.120	0.128
D2	2.35	2.55	0.093	0.100
E	3.10	3.50	0.122	0.138
E1	2.90	3.10	0.114	0.122
E2	1.64	1.84	0.065	0.072
e	0.65 BSC		0.026 BSC	
H	0.32	0.52	0.013	0.020
K	0.59	0.79	0.023	0.031
L	0.25	0.55	0.010	0.022

RECOMMENDED LAND PATTERN



UNIT: mm